

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In Re U.S. Patent Application )  
 Applicant: Shigeru Okamoto )  
 Serial No.: 09/521,389 )  
 Filed: March 8, 2000 )  
 For: EMBEDDED ELECTRO- )  
 CONDUCTIVE LAYER )  
 AND METHOD FOR )  
 FORMATION THEREOF )  
 Art Unit: 2823 )  
 Examiner: Eaton, K. )

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on this date.

8/7/01 James K. Folker  
 Date Registration No. 37,538  
 F-CLASS.WCM  
 Appr. February 20, 1998  
 Attorney for Applicant

RECEIVED  
 AUG 17 2001  
 2823  
 TECHNOLOGY CENTER 2800

RECEIVED  
 AUG 16 2001  
 Technology Center 2100

AMENDMENT TRANSMITTAL

Sir:

Transmitted herewith is a communication regarding the above-identified application.

Fee Calculation For Claims As Amended

	As Amended	Previously Paid For	Present Extra	Rate	Additional Fee
Total Claims	<u>7</u>	<u>20</u>	<u>= 0</u>	<u>x \$18.00</u>	<u>= \$ -0-</u>
Independent Claims	<u>2</u>	<u>6</u>	<u>= 0</u>	<u>x \$80.00</u>	<u>= \$ -0-</u>
Fee for Multiple Dependent Claims				<u>\$270.00</u>	<u>= \$ -0-</u>
					Total Additional Fee <u>\$ -0-</u>
					Small Entity Fee (reduced by half) <u>\$ -0-</u>

(X) Supplemental Amendment B (with Version with Markings to Show Changes Made).

(X) Change of Mailing Address.

(X) If a Petition under 37 C.F.R. §1.136(a) for an extension of time for response is required to make the attached response timely and does not separately accompany this transmittal, Applicant(s) hereby petition(s) under 37 C.F.R. §1.136(a) for an extension of time for response in the above-identified application for the period required to make the attached response timely.

(X) The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. §§1.16-1.17, or credit any overpayment, to Deposit Account No. 07-2069. A duplicate copy of this sheet is enclosed.

GREER, BURNS & CRAIN, LTD.

By: James K. Folker  
 James K. Folker, Reg. No. 37,538

August 7, 2001  
 300 South Wacker Drive-Suite 2500  
 Chicago, Illinois 60606  
 (312) 360-0080  
 Customer Number 24978  
 K:\150863671\SUP-B-TR.doc

1508.63671



PATENT APPLICATION

TECHNOLOGY CENTER

AUG 17 2001

21/11

R 800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U.S. Patent Application

Applicant: Shigeru Okamoto

Serial No.: 09/521,389

Filed: March 8, 2000

For: EMBEDDED ELECTRO-  
CONDUCTIVE LAYER  
AND METHOD FOR  
FORMATION THEREOF

Art Unit: 2823

Examiner: Eaton, K.

)  
I hereby certify that this paper is being deposited with  
the United States Postal Service as FIRST-CLASS mail in  
an envelope addressed to: Assistant Commissioner for  
Patents, Washington, D.C. 20231, on this date.

8/7/01

Date

F-CLASS.WCM

Appr. February 20, 1998

*Jane K. Tolson*  
Registration No. 37,538

Attorney for Applicant

SUPPLEMENTAL AMENDMENT B

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action dated February 8, 2001, and in addition to the amendments and arguments made in Amendment B, filed July 9, 2001, please additionally amend the above-identified Application as follows:

In the Claims:

Please amend Claims 1 and 5 as follows: